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ORIGINALLY FILED

Serial No. 09/910,117
Docket No. NEC G226



CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner of Patents & Trademarks, Washington, D.C. 20231 on August 22, 2002, at Tucson, Arizona.

By Diana Carr

NPS/nm

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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2816 #
#8/A
9/12/02
Hayes

In re Appln. Of: SAEKI
Serial No.: 09/910,117
Filed: July 20, 2001
For: CLOCK CONTROLLING METHOD AND CIRCUIT
Group: 2816
Examiner: LINH M. NGUYEN DOCKET: NEC G226

Assistant Commissioner of Patents & Trademarks
Washington, D.C. 20231

AMENDMENT B

Dear Sir:

This Amendment is being filed in response to the Official Action mailed May 22, 2002.

Please amend the Application as follows:

IN THE SPECIFICATION:

Please replace the paragraph bridging pages 1 and 2, beginning at page 1, line 22, to read as follows:

--For example, there is proposed in JP Patent Kokai JP-A-11-284497 a programmable delay generator in which a ramp waveform voltage for determining a delay time and a threshold voltage can be generated by circuits of the same structure and can be independently set so that it is capable of generating the delay time of a fractional number, a numerator and a denominator of which can be set, a frequency synthesizer which, by phase-interpolating output pulses of an accumulator using a programmable delay generator, is able to generate an adjustment-free low-spurious output signal, a multiplication circuit employing a programmable delay generator, a duty ratio converter circuit employing the programmable delay generator as an output pulse

AI

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